AMENDMENTS TO THE CLAIMS

1. (Currently Amended) A system, comprising:

a device that provides an output signal based on an input signal, the device having an associated bias; and

a bias device that provides a boost to the bias of the device based on the input signal[.] ;

wherein the bias device is further configured to provide maximum bias during the device's crossover time period.

2. (Original) The system of claim 1, further comprising a fixed biased device associated with the device, the bias device is connected in parallel to the fixed bias device.

3. (Original) The system of claim 1, the device is an amplifier.

4. (Original) The system of claim 3, the amplifier is a class AB amplifier.

5. (Cancelled)

6. (Original) The system of claim 1, the bias device modifies a bias current associated with the device.

7. (Currently Amended) [The system of claim 1] A system, comprising:

a device that provides an output signal based on an input signal, the device having an associated bias; and

a bias device that provides a boost to the bias of the device based on the input signal;

the bias device is further configured to provide maximum bias during the device's crossover time period.

wherein, the bias device further comprising a phase shifter that phase shifts the bias based on the input signal.

8. (Original) The system of claim 7, the phase shifter phase shifts the boost substantially ninety degrees from the input signal.

9. (Original) The system of claim 1, the bias device comprises a full-wave rectifier that provides a full-wave rectified current signal.

10. (Original) The system of claim 9, the bias device further comprises a phase shifter, the full-wave rectified current signal is phase shifted based on the input signal.

11. (Original) The system of claim 1, the bias device turning off during an absence of the input signal, and turning on during the presence of an input signal.

12. (Currently Amended) A system comprising:

means for providing a boost to a bias signal to a biased circuit; and

means for controlling the means for providing a boost, based on an input signal[.];

further comprising means for phase shifting the bias signal based on the input signal, the means for phase shifting configured to produce a maximum bias at a zero amplitude region.

13. (Original) The system of claim 12, further comprising means for providing a fixed bias associated with the biased circuit, the means for providing a fixed bias being in parallel with the means for producing a bias signal.

14. (Cancelled)

15. (Original) The system of claim 12, the means for controlling further comprising means for turning off the means for providing a boost in the absence of an input signal and for turning on the means for providing a boost in the presence of an input signal.

16. (Currently Amended) A method, comprising:

monitoring an input signal; and

modifying an amplifier bias based on the monitored input signal to provide a boost at a predetermined time[.];

further comprising phase shifting the amplifier bias based on the signal being amplified.

17. (Original) The method of claim 16, further comprising amplifying the input signal employing the modified bias.

18. (Original) The method of claim 16, the modifying further comprising enabling a boost in the presence of an input signal and discontinuing the boost after no input signal has been detected.

19. (Cancelled)

20. (Original) The method of claim 17, further comprising providing the maximum boost during the amplifier's crossover period.